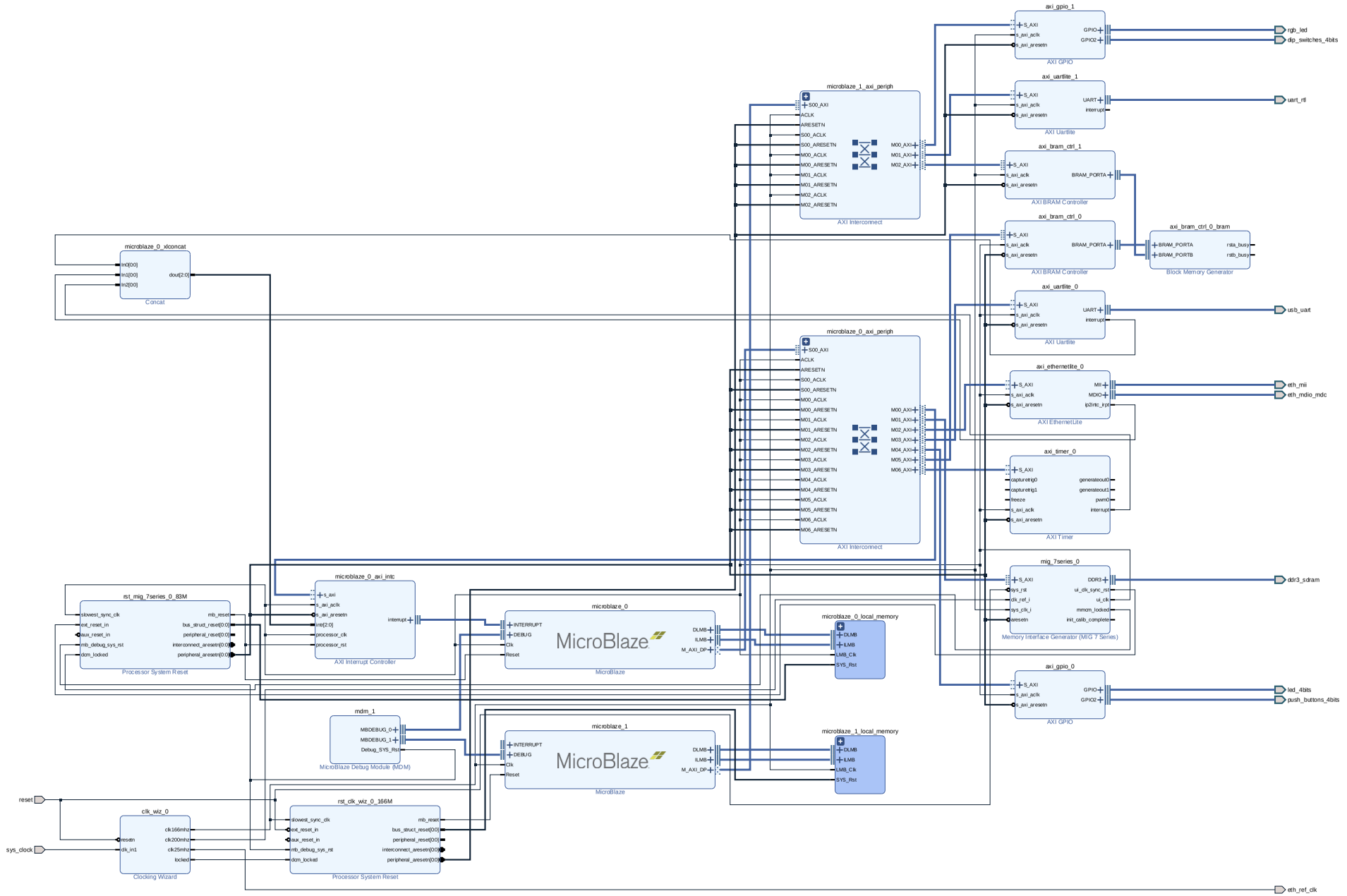
axi_gpio_1
S_AXI
s_axi_aclk
s_axi_aresetn
GPIO
GPIO2
AXI GPIO
rgb_led
dip_switches_4bits
axi_uartlite_1
UART
interrupt
AXI Uartlite
uart_rtl
microblaze_1_axi_periph
S00_AXI
ACLK
ARESETN
M00_AXI
M01_AXI
M02_AXI
AXI Interconnect
axi_bram_ctrl_1
BRAM_PORTA
AXI BRAM Controller
axi_bram_ctrl_0
axi_bram_ctrl_0_bram
BRAM_PORTB
rsta_busy
rstb_busy
Block Memory Generator
microblaze_0_xlconcat
In0[0:0]
In1[0:0]
In2[0:0]
dout[2:0]
Concat
axi_uartlite_0
usb_uart
microblaze_0_axi_periph
M03_AXI
M04_AXI
M05_AXI
M06_AXI
axi_ethernetlite_0
MII
MDIO
ip2intc_irpt
AXI EthernetLite
eth_mii
eth_mdio_mdc
axi_timer_0
capturetrig0
capturetrig1
freeze
generateout0
generateout1
pwm0
interrupt
AXI Timer
mig_7series_0
DDR3
ui_clk
ui_clk_sync_rst
mmcm_locked
init_calib_complete
Memory Interface Generator (MIG 7 Series)
ddr3_sdram
rst_mig_7series_0_83M
slowest_sync_clk
ext_reset_in
aux_reset_in
mb_debug_sys_rst
dcm_locked
mb_reset
bus_struct_reset[0:0]
peripheral_reset[0:0]
interconnect_aresetn[0:0]
peripheral_aresetn[0:0]
Processor System Reset
microblaze_0_axi_intc
s_axi
s_axi_aclk
s_axi_aresetn
intr[2:0]
processor_clk
processor_rst
AXI Interrupt Controller
microblaze_0
INTERRUPT
DEBUG
Clk
Reset
DLMB
ILMB
M_AXI_DP
MicroBlaze
microblaze_0_local_memory
DLMB
ILMB
LMB_Clk
SYS_Rst
axi_gpio_0
led_4bits
push_buttons_4bits
mdm_1
MBDEBUG_0
MBDEBUG_1
Debug_SYS_Rst
MicroBlaze Debug Module (MDM)
microblaze_1
microblaze_1_local_memory
reset
sys_clock
clk_wiz_0
resetn
clk_in1
clk166mhz
clk200mhz
clk25mhz
locked
Clocking Wizard
rst_clk_wiz_0_166M
eth_ref_clk